

EXPRESS MAIL MAILING LABEL NO. EL 830 830 516 U'S



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Brent Keeth
Pierre C. Fazan

Group Art Unit: 2814

Examiner: Wille, Douglas A.

Serial No.: 08/530,661

Atty. Dkt. No.: 11762.0266.NPUS00

Filed: 09/20/1995

For: SEMICONDUCTOR MEMORY
CIRCUITRY

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents are enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Toshiaki et al. U.S. Patent 5,555,212 and Eimori et al., "A Newly Designed Planar Stacked Capacitor Cell with High Dielectric Constant Film for 256Mbit DRAM," IEDM 1993 pp. 26.3.1-4, were recently cited by the examiner in application Ser. 08/603,471 filed Feb. 20, 1996, which claims priority of the present application.

Keeth et al. Serial No.: 08/530,661

Respectfully submitted.

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